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## \*BIBDATASHEET\*

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**CONFIRMATION NO. 4755** 

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APPLICANTS									
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** CONTINUING DATA **********************************									
** FOREIGN APPLICATIONS ************************************									
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Foreign Priority claimed $y_{yes} \times n_0$ 35 USC 119 (a-d) conditions $y_{yes} \times n_0 $ Met after				STATE OR	SHE	SHEETS TOT		AL	INDEPENDENT
met Allowance Allowance Allowance Examiner's Signature Initials				COUNTRY JAPAN		DRAWING CLA			CLAIMS 7
ADDRESS 021567 WELLS ST. JOHN P.S. 601 W. FIRST AVENUE, SUITE 1300 SPOKANE, WA 99201									
TITLE  Methods of forming an isolation trench in a semiconductor, methods of forming an isolation trench in a surface of a silicon wafer, methods of forming an isolation trench-isolated transistor, trench-isolated transistor, trench isolation structures formed in a semiconductor, memory cells and DRAMs									
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